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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/753,006	12/29/2000	Joseph A. Bennett	42390.P9942	9847

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EXAMINER

LE, DIEU MINH T

ART UNIT

PAPER NUMBER

2184

DATE MAILED: 08/14/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/753,006

Applicant(s)

BENNETT, JOSEPH A.

Examiner

Dieu-Minh Le

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 April 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

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Part III DETAILED ACTION

Specification

1. Claims 1-16 are presented for examination.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -
(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-16 are rejected under 35 U.S.C. § 102(b) as being anticipated by Egan et al. (US Patent 5,875,308 hereafter referred to as Egan).

As per claim 1:

Egan explicitly teaches:

- a system

comprising:

- a bus including power line [col. 2, lines 17-27];
- a bus bridge device including an internal logic unit [col. 3, lines 1-11 and lines 47-60] and;

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- a power regulator to delivery power to the power lines [col. 3, lines 66 through col. 4, lines 17 and col. 4, lines 60 through col. 5, lines 2];
- the power regulator [col. 3, lines 66 through col. 4, lines 17 and col. 4, lines 60 through col. 5, lines 2] to assert a fault signal to the bus bridge device if a power fault is detected [col. 3, lines 59.

This is clearly shown that Egan's enhanced peripheral component interconnect (PCI) architecture for a data processing system does illustrate, demonstrate, and teach capabilities corresponded to Applicant's invention.

As per claims 2-5:

Egan explicitly teaches:

- a system

comprising:

- a bus bridge device to disconnect the internal logic unit from the bus in response to an assertion of the fault signal [col. 3, lines 47-60 and col. 4, lines 1-10];
- a power regulator to cease to deliver power to the power line if a power fault is detected [col. 3, lines 66 through col. 4, lines 17 and col. 4, lines 60 through col. 5, lines 2];

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- a bus bridge device to assert an interrupt signal in response to the assertion of the fault signal [col. 4, lines 18-37];
- the bus bridge device to assert an error signal in response to the assertion of the fault signal (i.e., bus bridge power monitoring input/notification) [col. 4, lines 38-53].

This is clearly shown that Egan's enhanced peripheral component interconnect (PCI) architecture for a data processing system does illustrate, demonstrate, and teach capabilities corresponded to Applicant's invention.

As per claims 6-9:

Egan explicitly teaches:

- a system
- comprising:
- a bus bridge device to assert a power enable (i.e., turn power on) signal to the power regulator [col. 3, lines 66 through col. 4, lines 17 and col. 4, lines 60 through col. 5, lines 2] upon system startup [col. 4, lines 2-17];
 - the bus bridge deassert the power enable signal follow the assertion of the fault signal [col. 3, lines 66 through col. 4, lines 53];

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- a power regulator module to deassert the fault signal in response to the deassertion of the power enable signal [col. 3, lines 66 through col. 4, lines 59].
- the bus is a PCI bus [col. 2, lines 59-63].

This is clearly shown that Egan's enhanced peripheral component interconnect (PCI) architecture for a data processing system does illustrate, demonstrate, and teach capabilities corresponded to Applicant's invention.

As per claims 10-12:

These claims are the same as per claims 1-9. The only minor different is that these claims are directed to **a bus bridge device** instead of the system comprising a bus bridge device as described in claims 1-9. However, Egan's enhanced peripheral component interconnect (PCI) architecture for a data processing explicitly teaches a bus bridge device [col. 3, lines 1-11 and lines 47-60]. Therefore, these claims are also rejected under the same rationale applied against claims 1-9.

As per claims 13-16:

Due to the similarity of claims 13-16 to claims 1-9 except for a method comprising applying power step, detecting power step, asserting a fault signal step, etc... instead a system

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comprising capabilities of detecting power, deliver power, asserting fault signal, etc...therefore, these claims are also rejected under the same rationale applied against claims 1-9. In addition, all of the limitations have been noted in the rejection as per claims 1-9.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

4. A shortened statutory period for response to this action is set to expired THREE (3) months, ZERO days from the date of this letter. Failure to respond within the period for response will cause the application to be abandoned. 35 U.S.C. 133.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dieu-Minh Le whose telephone number is (703) 305-9408. The examiner can normally be reached on Monday-Thursday from 6:30 AM to 4:00 PM. The examiner can also be reached on alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel, can be reached on (703)305-9713. The fax phone number for this Group is (703)746-7240.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3900.

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Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(703) 746-7239, (for formal communications
intended for entry)

Or:

(703) 746-7240 (for informal or draft
communications, please label "PROPOSED" or
"DRAFT")

Hand-delivered responses should be brought to Crystal
Park II, 2121 Crystal Drive, Arlington. VA., Sixth
Floor (Receptionist).



**DIEU-MINH THAI LE
PRIMARY EXAMINER
ART UNIT 2184**

DML
8/9/03